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FORM PTO-1

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. 93-C-077C1 (STMI01-93077)

SERIAL NO. 09/712,827

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT Loi N. Nguyen, et al.

FILING DATE

GROUP

		FILING DATE November 14,		November 14, 2	2000	-2835			
<u></u>			σ	. S. PATEN	r DOCUMENTS				
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLASS	FILING DATE (if appropriate)	
LY	AA	4,686,000	08/11/1987	Heath		156	643	Feb. 19, 1986	
	AB	4,824,767	04/25/1989	Chambers,	et al.	430	313	Jun. 2, 1987	
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LP	AH	0 523 856 A2	06/28/91	Europe					
1	ΑI	0 534 130 A1	3/31/93	Europe				<u> </u>	
120	AJ	JP60246675	06/12/85	Japan			/		
LP.	AK	JP4196486	07/16/92	Japan			1		
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	MA								
		OTHER RE	FERENCES (Inc	luding Autho	r, Title, Date, Pe	rtinent Pages, E	tc.)		
B	AN	"VLSI Fabrication Principles," by Sorab K. Ghandhi, Rensselaer Polytechnic Institute, A Wiley Publication, John Wiley & Sons, 1983, pages 479-482, 499-501.							
11	АО								
	AP								
EXAMINER		Long f	Pham		DATE CONSIDER	2/7/	12.		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line if not in conformance and not considered. Include copy of this form with next communication to applicant.